

Express Mail Number: EV327170240US

Date of Mailing: March 22, 2004

Our Case No. 10519-116

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
March et al.	)	
	)	Group Art Unit: Not Yet Assigned
Serial No.: Not Yet Assigned	)	
	)	Examiner: Not Yet Assigned
Filed: Herewith	)	
	)	
For: Configuring File Structures and	)	
File System Structures in a	)	
Memory Device	)	

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Pursuant to the obligation under 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97-1.99, Applicants hereby submit documents A1-A91 listed on the attached form PTO-1449 for consideration by the Examiner. In accordance with 37 C.F.R. § 1.98(d), copies of these documents are not enclosed since they were previously cited by or submitted to the Office in prior application serial number 10/253,049, which is being relied upon for an earlier filing date under 35 U.S.C. § 120.

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, Applicants reserve the right to contest that any of the information submitted herewith is prior art against the present application.

Applicants wish to bring the following to the Examiner's attention:

Page 291 of the "Linux System Administration" document and page 408 of the "Undocumented DOS" document teach the use of two partitions on a single hard drive. The partitions can have different operating systems (e.g., DOS and Unix) and different file systems (e.g., DOS and Unix), each with different smallest writeable units, as recorded in the logical sector size. The same applies to two separate hard drives. This is an example of two different re-writeable memories with different smallest writeable units sending an indication of their respective smallest writeable units to a file system.

Page 408 of the "Undocumented DOS" document also teaches a memory device with two partitions, each with a different file system structure. Here, a PC hard drive is partitioned into two, with a Unix file system on one partition and a DOS file system on the other. DOS allows such a partition.

Page 410 of the "Undocumented DOS" document teaches that both DOS and Unix have a boot sector, which is stored in a hard disk at a memory location reserved for file system structures.

Page 413 of the "Undocumented DOS" document teaches a file system (e.g., DOS FAT or Unix) allocating blocks (its smallest writeable unit) and keeping track of which blocks are free.

Page 288 of the "Universal Serial Bus Specification" document teaches the use of a driver to take care of variability of the smallest writeable unit of a memory device. Here, the Universal Serial Bus (USB) driver is used in PCs to allow a variety of memory devices of different types to be accessed.

The "UNIX chmod command" document teaches the Unix "chmod" command to set file attributes to change file permissions. This can be used to prevent users from writing to a file. In this way, a re-writeable media (such as a hard disk) can apply a modifiable bit (such as a read-only or archive bit) to temporarily close a file or partition. DOS, Windows, and Unix all have such a bit to prevent a file from being over-written.

Page 25, section 2.2.4.2 specifies the logical block size and what its maximum may be in any given logical volume. A minimum block size is assumed. The block size is sent from the write-once memory (here, a CD-ROM) to the Universal Disk Format file system.

Dated: March 22, 2004

Respectfully submitted,



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FORM PTO-1449	SERIAL NO. Not Yet Assigned	CASE NO. 10519/116
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE Herewith	GROUP ART UNIT Not Yet Assigned
(use several sheets if necessary)		APPLICANT(S): MARCH ET AL.

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	4,646,266	02/24/87	OVSHINSKY ET AL.		
	A2	6,236,587	05/22/01	GUDESEN ET AL.		
	A3	6,034,882	03/07/00	JOHNSON ET AL.		
	A4	5,835,396	11/10/98	ZHANG		
	A5	5,784,391	07/21/98	KONIGSBURG		
	A6	5,469,450	11/21/95	CHO ET AL.		
	A7	5,313,425	05/17/94	LEE ET AL.		
	A8	6,208,545	03/27/01	LEEDY		
	A9	5,559,732	09/24/96	BIRGE		
	A10	5,915,167	06/22/99	LEEDY		
	A11	6,321,358	11/2001	ANDERSON		
	A12	6,108,236	8/2000	BARNETT		
	A13	6,052,816	4/2000	YOSHINOAWA		
	A14	5,469,451	11/1995	HENMI		
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	A16	6,185,122	2/2001	JOHNSON ET AL.		
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	A18	5,943,254	8/1999	BAKEMAN, JR. ET AL.		
	A19	5,835,509	11/1998	SAKO ET AL.		
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	A24	6,321,360 B1	11/2001	Takeuchi et al.		
	A25	6,377,526 B1	04/2002	Vining et al.		
	A26	6,480,463 B2	11/2002	Hunter et al.		
	A27	6,336,175 B1	01/2002	Shaath et al.		
	A28	6,446,177	09/2002	Tanaka et al.		
	A29	US 2002/0034105 A1	03/2002	Kulkarni et al.		
	A30	5,559,778	09/1996	Inokuchi et al.		
	A31	5,119,291	06/1992	Flannagan et al.		
	A32	5,321,824	06/1994	Burke et al.		
	A33	5,448,728	09/1995	Takano et al.		
	A34	5,761,741	06/1998	Robbins et al.		
	A35	5,437,028	07/1995	Iijima		
	A36	5,890,169	03/1999	Wong et al.		
	A37	6,000,023	12/1999	Jeon		

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A38	5,043,967	08/1991	Gregg et al.		
	A39	5,029,125	07/1991	Sciupac		
	A40	6,530,009 B1	03/2003	James		
	A41	6,446,073 B1	09/2002	D'Amato et al.		
	A42	6,282,605 B1	08/2001	Moore		
	A43	6,226,241 B1	05/2001	D'Amato et al.		

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
	A44	WO 99/14763	08/28/98	PCT		
	A45	EP 1 017 100 A1	7/2000	EP		
	A46	EP 0 073 486 A2	3/1983	EP		
	A47	EP 1 168 150 A1	02/2002	EPO		

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
	A48	"New Direct CD 2.5 Improves Speed and Reliability of Data Archiving Using CD-R/CD-RW," <a href="http://www.adaptec.com/worldwide/pressrelease.html?prodkey=08051998">http://www.adaptec.com/worldwide/pressrelease.html?prodkey=08051998</a> , 2 pages, August 5, 1998.
	A49	"Universal Disk Format (UDF) Driver," <a href="http://www.trylinux.com/projects/udf/index.html">http://www.trylinux.com/projects/udf/index.html</a> , 3 pages (1999).
	A50	"FAT File Allocation Table," <a href="http://www.easydesksoftware.com/fat.htm">http://www.easydesksoftware.com/fat.htm</a> , 2 pages, October 24, 1999.
	A51	"FAT File Allocation Table," <a href="http://www.oreilly.com/reference/directory/terms/F/File_Allocation_Table.htm">http://www.oreilly.com/reference/directory/terms/F/File_Allocation_Table.htm</a> , 4 pages (1996).
	A52	"ISO9960 Simplified for DOS/Windows by Philip J. Erdelsky," <a href="http://www.alumni.caltech.edu/~pje/iso9660.html">http://www.alumni.caltech.edu/~pje/iso9660.html</a> , 8 pages August 26, 2000.
	A53	"Memory cards: designing with a full deck," <a href="http://www.ednmag.com/ednmag/reg/2000/052520000/11dfl.htm">http://www.ednmag.com/ednmag/reg/2000/052520000/11dfl.htm</a> , 12 pages, May 25, 2000.
	A54	"DOS Disk Formats," <a href="http://www.qvctc.commnet.edu/classes/csc277/formats.html">http://www.qvctc.commnet.edu/classes/csc277/formats.html</a> , 7 pages (1999).
	A55	"MS-DOS Partitioning Summary," <a href="http://www.qvctc.commnet.edu/classes/csc277/fdisk2.html">http://www.qvctc.commnet.edu/classes/csc277/fdisk2.html</a> , 3 pages (1999).
	A56	"DOS Partitions," <a href="http://www.qvctc.commnet.edu/classes/csc277/partitions.html">http://www.qvctc.commnet.edu/classes/csc277/partitions.html</a> , 5 pages (1999).

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	A57	"DOS Disk Structures-Definitions," <a href="http://www.qvctc.commnet.edu/classes/csc277/formats2.html">http://www.qvctc.commnet.edu/classes/csc277/formats2.html</a> , 3 pages (1999).
	A58	"MS-DOS Summary A Survival Guide," <a href="http://www.washtenaw.cc.mi.us/dept/cis/mod/q02cd.htm">http://www.washtenaw.cc.mi.us/dept/cis/mod/q02cd.htm</a> , 10 pages (1996).
	A59	"Hardware White Paper, FAT: General Overview of On-Disk Format," Microsoft Corp., 25 pages, May 5, 1999.
	A60	"Method for Deleting Stored Digital Data from Write-Once Memory Device," U.S. Patent Application Serial No. 09/638,439, filed August 14, 2000; inventors: Christopher S. Moore, Derek J. Bosch, Daniel C. Steere, and J. James Tringali.
	A61	"Method for Storing Digital Information in Write-Once Memory Array," U.S. Patent Application Serial No. 09/727,229, filed November 30, 2000; inventors: David R. Friedman Derek J. Bosch, Christopher S. Moore, J. James Tringali, and Michael A. Vvyoda.
	A62	"Three-Dimensional Memory Array and Method of Fabrication," U.S. Patent Application Serial No. 09/560,626, filed April 28, 2000; inventor: Johan Knall.
	A63	"Write-Once Memory Array Controller, System, and Method," U.S. Patent Application Serial No. 09/638,427, filed August 14, 2000; inventors: Derek J. Bosch, Christopher S. Moore, Daniel C. Steere, and J. James Tringali.
	A64	"Low-Cost Three-Dimensional Memory Array," U.S. Patent Application Serial No. 09/638,428, filed August 14, 2000; inventors: Mark G. Johnson, Thomas H. Lee, Vivek Subramanian, and P. Michael Farmwald.
	A65	"Modular Memory Device," U.S. Patent Application Serial No. 09/638,334, filed August 14, 2000; inventors: J. James Tringali, P. Michael Farmwald, Thomas H. Lee, Mark G. Johnson, and Derek J. Bosch.
	A66	"The MS-DOS Filesystem," <a href="http://www.cs.adfa.oz.au/teaching/studinfo/osrts/Lectures/node113.html">http://www.cs.adfa.oz.au/teaching/studinfo/osrts/Lectures/node113.html</a> , 2 pages March 1, 2001.
	A67	"Computer Engineering: Hardware Design," M. Morris Mano, Chapter 6-4 Error Detection and Correction, pages 199-202 (1988).
	A68	"Symantec The Norton Desktop User's Guide," Chapter 16 "Using Shredder," 8 pages (1993).
	A69	"Reed-Solomon Codes," <a href="http://www.4i2i.com/reed_solomon_codes.htm">http://www.4i2i.com/reed_solomon_codes.htm</a> , 8 pages (1998).
	A70	"Linux System Administration — White Papers," Kirch et al., pages 291-292 (1996).
	A71	"Undocumented DOS Second Edition — A Programmer's Guide to Reserved MS-DOS® Functions and Data Structures," Schulman et al., pages 408-413 (1994).
	A72	"Universal Serial Bus Specification — Revision 2.0," pages 287-289 (April 27, 2000).
	A73	"UNIX chmod command," <a href="http://www.psc.edu/general/unix/chmod.html">http://www.psc.edu/general/unix/chmod.html</a> , 1 page (November 13, 1999).
	A74	"Universal Disk Format® Specification — Revision 2.01," Optical Storage Technology Association, page 25 (March 15, 2000).
	A75	"Jack St. Clair Kilby," Jones Telecommunications & Multimedia Encyclopedia, <a href="http://www.digitalcentury.com/encyclo/update/kilby.html">http://www.digitalcentury.com/encyclo/update/kilby.html</a> , 4 pages (1999).

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	A76	"The 'Chip' Inventors (part 1)," Dan Murray, <a href="http://www.livingstonmontana.com/access/dan/150thechipinventors-1.html">http://www.livingstonmontana.com/access/dan/150thechipinventors-1.html</a> , 3 pages (1999).
	A77	T. R. Reid, "The Chip: How Two Americans Invented the Microchip and Launched a Revolution," pages 22-23 (1984).
	A78	"About Jack" <a href="http://www.ti.com/corp/docs/kilbyctr/jackbuilt.shtml">http://www.ti.com/corp/docs/kilbyctr/jackbuilt.shtml</a> , 6 pages (2002).
	A79	"Definition: integrated circuit," <a href="http://www.its.bldrdoc.gov/fs-1037/dir-019/2755.htm">http://www.its.bldrdoc.gov/fs-1037/dir-019/2755.htm</a> , 1 page (1996).
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	A83	"Jack St. Clair Kilby," Jones Telecommunications & Multimedia Encyclopedia, <a href="http://www.digitalcentury.com/encyclo/update/kilby.html">http://www.digitalcentury.com/encyclo/update/kilby.html</a> , 4 pages (1999).
	A84	"The 'Chip' Inventors (part 1)," Dan Murray, <a href="http://www.livingstonmontana.com/access/dan/150thechipinventors-1.html">http://www.livingstonmontana.com/access/dan/150thechipinventors-1.html</a> , 3 pages (1999).
	A85	T.R. Reid, "The Chip: How Two Americans Invented the Microchip and Launched a Revolution," pages 22-23 (1984).
	A86	"About Jack" <a href="http://www.ti.com/corp/docs/kilbyctr/jackbuilt.shtml">http://www.ti.com/corp/docs/kilbyctr/jackbuilt.shtml</a> , 6 pages (2002).
	A87	"Definition: integrated circuit," <a href="http://www.its.bldrod.gov/fs-1037/dir-019/2755.htm">http://www.its.bldrod.gov/fs-1037/dir-019/2755.htm</a> , 1 page (1996).
	A88	Meindl, "Definitions of Terms for Integrated Electronics," IEEE Journal of Solid-State Circuits, page 2 (March 1967)
	A89	Muller et al., "Device Electronics for Integrated Circuits," second edition, pages 65 and 106 (1986).
	A90	"3D-ROM – A First Practical Step Towards 3D-IC," Zhang, 7 pages, July 2000.
	A91	"Construction Techniques for Systematic SEC-DED Codes with Single Byte Error Detection and Partial Correction Capability for Computer Memory Systems," Penzo et al., IEEE Transactions on Information Theory, Vol. 41, No. 2, March 1995, pages 584-591.

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